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 P.O. Box 1450
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Inventor(s): Manuel J. Alvarez II; Thomas A. Dye; Peter Geiger
 Assignee: Interactive Silicon, Inc.
 Title: System And Method For Recognizing And Configuring Devices Embedded
 On Memory Modules
 Serial No.: 09/840,724
 Docket No.: 40532-P008US (f/k/a 5143-02400)

ENCLOSED:

- X This return receipt postcard
- 2 pages – Transmittal letter
- 2 pages – Information Disclosure Statement Under 37 CFR § 1.97(b)
- 1 page – PTO Form 1449
- X Copies of cited references in PTO Form 1449 (A1-A5)
- 12 pages – Revocation of Prior and Substitute Power of Attorney

MPA/wk
 July 14, 2003

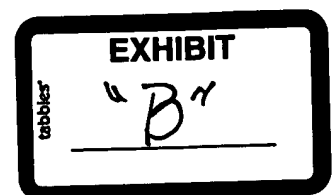
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Title: System And Method For Recognizing And Configuring Devices Embedded On
Memory Modules
Serial No.: 09/840,724 Filing Date: 4/23/01
Examiner: Unknown Group Art Unit: Unknown
Winstead Docket No.: 40532-P008US (f/k/a 5143-02400)

Transmitted herewith are the following:

1. Return Receipt Postcard;
2. This Transmittal Letter;
3. 2 pages of Information Disclosure Statement Under 37 CFR § 1.97(b);
4. 1 page of PTO Form 1449;
5. Copies of cited references in PTO Form 1449 (A1-A5); and
6. 12 pages of Revocation of Prior and Substitute Power of Attorney.

The enclosed Revocation of Prior and Substitute Power of Attorney is a copy. The original Revocation of Prior and Substitute Power of Attorney is located in the file for U.S. Patent No. 6,518,965 B2, and the public is authorized to inspect and copy the original paper in the event one of the applications listed in Exhibit A containing a copy hereof matures into or is already an issued patent.

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence (along with any item referred to as being enclosed herewith) is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop DD, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 14, 2003.



Signature

Respectfully submitted,



Michael P. Adams
Attorney for Applicant(s)
Reg. No. 34,763

MPA:wk
Enclosure(s)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application(s) / Patent(s) of:

INTERACTIVE SILICON, INC.

Dallas, Texas
March 26, 2003

COMMISSIONER FOR PATENTS
Washington, DC 20231

Dear Sir:

REVOCATION OF PRIOR AND SUBSTITUTE POWER OF ATTORNEY

I hereby revoke any and all prior Powers of Attorney executed in connection with the Patent Application(s) and/or Granted Patent(s) specified on the attached Exhibit A, and hereby appoint:

Michael P. Adams
Reg. No. 34,763

Barry Newberger
Reg. No. 41,527

my attorneys with full power of substitution and revocation, to prosecute the application(s) and patent(s) and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent application(s) filed thereon before any international authorities under the Patent Cooperation Treaty.

Please refer to our file numbers specified on the attached Exhibit A.

Please send all correspondence to the address listed below:

Michael P. Adams
Winstead Sechrest & Minick P.C.
P.O. Box 50784
5400 Renaissance Tower
1201 Elm Street
Dallas, Texas 75250-0784
Telephone (512) 474-4330
Facsimile (512) 370-2850

Date: 3-25-03

INTERACTIVE SILICON, INC.


By: 
Printed: Kenneth East
Title: President & Chief Executive Officer

EXHIBIT A

- | | | |
|----|------------------------|--|
| 1. | Serial No: | 08/340,667 |
| | Filing Date: | 11/16/94 |
| | Patent No.: | 6,002,411 |
| | Issue Date: | 12/14/99 |
| | Title: | INTEGRATED VIDEO AND MEMORY
CONTROLLER WITH DATA PROCESSING
AND GRAPHICAL PROCESSING
CAPABILITIES |
| | Assignment Reel/Frame: | 9275/0387 |
| | Our File No.: | 40532-P001X1 |
| | Former File No.: | 5143-00101 |
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- | | | |
|----|------------------------|--|
| 2. | Serial No: | 09/241,139 |
| | Filing Date: | 2/1/99 |
| | Patent No.: | 6,370,631 |
| | Issue Date: | 4/9/02 |
| | Title: | MEMORY CONTROLLER INCLUDING
EMBEDDED COMPRESSION /
DECOMPRESSION CAPABILITIES FOR
IMPROVED DATA ACCESS BETWEEN NON-
VOLATILE AND SYSTEM MEMORY |
| | Assignment Reel/Frame: | 9758/0602 |
| | Our File No.: | 40532-P001D2 |
| | Former File No.: | 5143-00202 |
-

3.	Serial No:	09/461,172
	Filing Date:	12/14/99
	Patent No.:	6,170,047
	Issue Date:	1/2/01
	Title:	SYSTEM AND METHOD FOR MANAGING SYSTEM MEMORY AND/OR NON- VOLATILE MEMORY USING A MEMORY CONTROLLER WITH INTEGRATED COMPRESSION AND DECOMPRESSION CAPABILITIES
	Our File No.:	40532-P001D3
	Former File No.:	5143-00203

4.	Serial No:	08/916,464
	Filing Date:	8/8/97
	Patent No.:	6,173,381
	Issue Date:	1/9/01
	Title:	MEMORY CONTROLLER INCLUDING EMBEDDED DATA COMPRESSION AND DECOMPRESSION ENGINES
	Our File No.:	40532-P001D1X2
	Former File No.:	5143-00204

5.	Serial No:	08/565,103
	Filing Date:	11/30/95
	Patent No.:	5,838,334
	Issue Date:	11/17/98
	Title:	MEMORY AND GRAPHICS CONTROLLER WHICH PERFORMS POINTER-BASED DISPLAY LIST VIDEO REFRESH OPERATIONS
	Assignment Reel/Frame:	9275/0371
	Our File No.:	40532-P001P1C1
	Former File No.:	5143-00301

6.	Serial No:	08/604,670
	Filing Date:	2/21/96
	Patent No.:	5,995,120
	Issue Date:	11/30/99
	Title:	GRAPHICS SYSTEM INCLUDING A VIRTUAL FRAME BUFFER WHICH STORES VIDEO/PIXEL DATA IN A PLURALITY OF MEMORY AREAS
	Our File No.:	40532-P001M1X1
	Former File No.:	5143-00304

7.	Serial No:	08/770,017
	Filing Date:	12/19/96
	Patent No.:	6,108,014
	Issue Date:	8/22/00
	Title:	SYSTEM AND METHOD FOR SIMULTANEOUSLY DISPLAYING A PLURALITY OF VIDEO DATA OBJECTS HAVING DIFFERENT BIT PER PIXEL FORMATS
	Our File No.:	40532-P001M2X1
	Former File No.:	5143-00305

8.	Serial No:	09/971,107
	Filing Date:	10/4/01
	Patent No.:	6,518,965 B2
	Issue Date:	2/11/03
	Title:	GRAPHICS SYSTEM AND METHOD FOR RENDERING INDEPENDENT 2D AND 3D OBJECTS USING POINTER BASED DISPLAY LIST VIDEO REFRESH OPERATIONS
	Assignment Reel/Frame:	012505/0682
	Our File No.:	40532-P021C1
	Former File No.:	5143-01103

9.	Serial No:	09/056,021
	Filing Date:	4/6/98
	Patent No.:	6,067,098
	Issue Date:	5/23/00
	Title:	VIDEO/GRAPHICS CONTROLLER WHICH PERFORMS POINTER-BASED DISPLAY LIST VIDEO REFRESH OPERATIONS
	Assignment Reel/Frame:	9274/0804
	Our File No.:	40532-P001M3P1
	Former File No.:	5143-01200

10.	Serial No:	10/085,274
	Filing Date:	2/28/02
	Title:	VIDEO CONTROLLER SYSTEM WITH SCREEN CACHING
	Assignment Reel/Frame:	012663/0652
	Our File No.:	40532-P003C1
	Former File No.:	5143-01501

11.	Serial No:	09/239,659
	Filing Date:	1/29/99
	Title:	BANDWIDTH REDUCING MEMORY CONTROLLER INCLUDING SCALABLE EMBEDDED PARALLEL DATA COMPRESSION AND DECOMPRESSION ENGINES
		***Response to Office Action submitted on 8/5/02 requested title change to: SELECTIVE LOSSLESS, LOSSY, OR NO COMPRESSION OF DATA BASED ON ADDRESS RANGE, DATA TYPE, AND/OR REQUESTING AGENT
	Assignment Reel/Frame:	9742/0780
	Our File No.:	40532-P001M4P1
	Former File No.:	5143-01700

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| 18. | Serial No: | 09/550,380 |
| | Filing Date: | 4/14/00 |
| | Patent No.: | 6,523,102 B1 |
| | Issue Date: | 2/18/03 |
| | Title: | PARALLEL COMPRESSION /
DECOMPRESSION SYSTEM AND METHOD
FOR IMPLEMENTATION OF IN-MEMORY
COMPRESSED CACHE IMPROVING
STORAGE DENSITY AND ACCESS SPEED
FOR INDUSTRY STANDARD MEMORY
SUBSYSTEMS AND IN-LINE MEMORY
MODULES |
| | Assignment Reel/Frame: | 010730/0424 |
| | Our File No.: | 40532-P007US |
| | Former File No.: | 5143-02000 |
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- | | | |
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| 19. | Serial No: | 09/840,724 |
| | Filing Date: | 4/23/01 |
| | Title: | SYSTEM AND METHOD FOR RECOGNIZING
AND CONFIGURING DEVICES EMBEDDED
ON MEMORY MODULES |
| | Assignment Reel/Frame: | 011755/0793 |
| | Our File No.: | 40532-P008US |
| | Former File No.: | 5143-02400 |
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- | | | |
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| 20. | Serial No: | 09/915,751 |
| | Filing Date: | 7/26/01 |
| | Title: | SYSTEM AND METHOD FOR MANAGING
COMPRESSION AND DECOMPRESSION OF
SYSTEM MEMORY IN A COMPUTER
SYSTEM |
| | Assignment Reel/Frame: | 012493/0726 |
| | Our File No.: | 40532-P009US |
| | Former File No.: | 5143-02501 |
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| 21. | Serial No: | 10/205,590 |
| | Filing Date: | 7/25/02 |
| | Title: | PARALLEL COMPRESSION METHOD FOR
IMPROVING NETWORK PROCESSING BY
IMPROVING STORAGE DENSITY AND
ACCESS SPEED WITHIN NETWORK DATA
PROCESSING SWITCHES |
| | Our File No.: | 40532-P010US |
| | Former File No.: | 5143-02801 |
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- | | | |
|-----|------------------------|---|
| 22. | Serial No: | 10/044,786 |
| | Filing Date: | 1/11/02 |
| | Title: | PARALLEL COMPRESSION AND
DECOMPRESSION SYSTEM AND METHOD
HAVING MULTIPLE PARALLEL
COMPRESSION AND DECOMPRESSION
ENGINES |
| | Assignment Reel/Frame: | 012694/0922 |
| | Our File No.: | 40532-P001M10P2 |
| | Former File No.: | 5143-03000 |
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|-----|------------------------|---|
| 23. | Serial No: | 10/044,785 |
| | Filing Date: | 1/11/02 |
| | Title: | SYSTEM AND METHOD FOR GENERATING
OPTIMALLY COMPRESSED DATA FROM A
PLURALITY OF DATA COMPRESSION /
DECOMPRESSION ENGINES
IMPLEMENTING DIFFERENT DATA
COMPRESSION ALGORITHMS |
| | Assignment Reel/Frame: | 012704/0236 |
| | Our File No.: | 40532-P001M11P3 |
| | Former File No.: | 5143-03100 |
-

24. Serial No: 10/227,607
 Filing Date: 8/23/02
 Title: MANAGING A CODEC ENGINE FOR
 MEMORY COMPRESSION /
 DECOMPRESSION OPERATIONS USING A
 DATA MOVEMENT ENGINE

 Our File No.: 40532-P014US
 Former File No.: 5143-03601

AUSTIN_1\210308\1
40532-1 03/24/2003